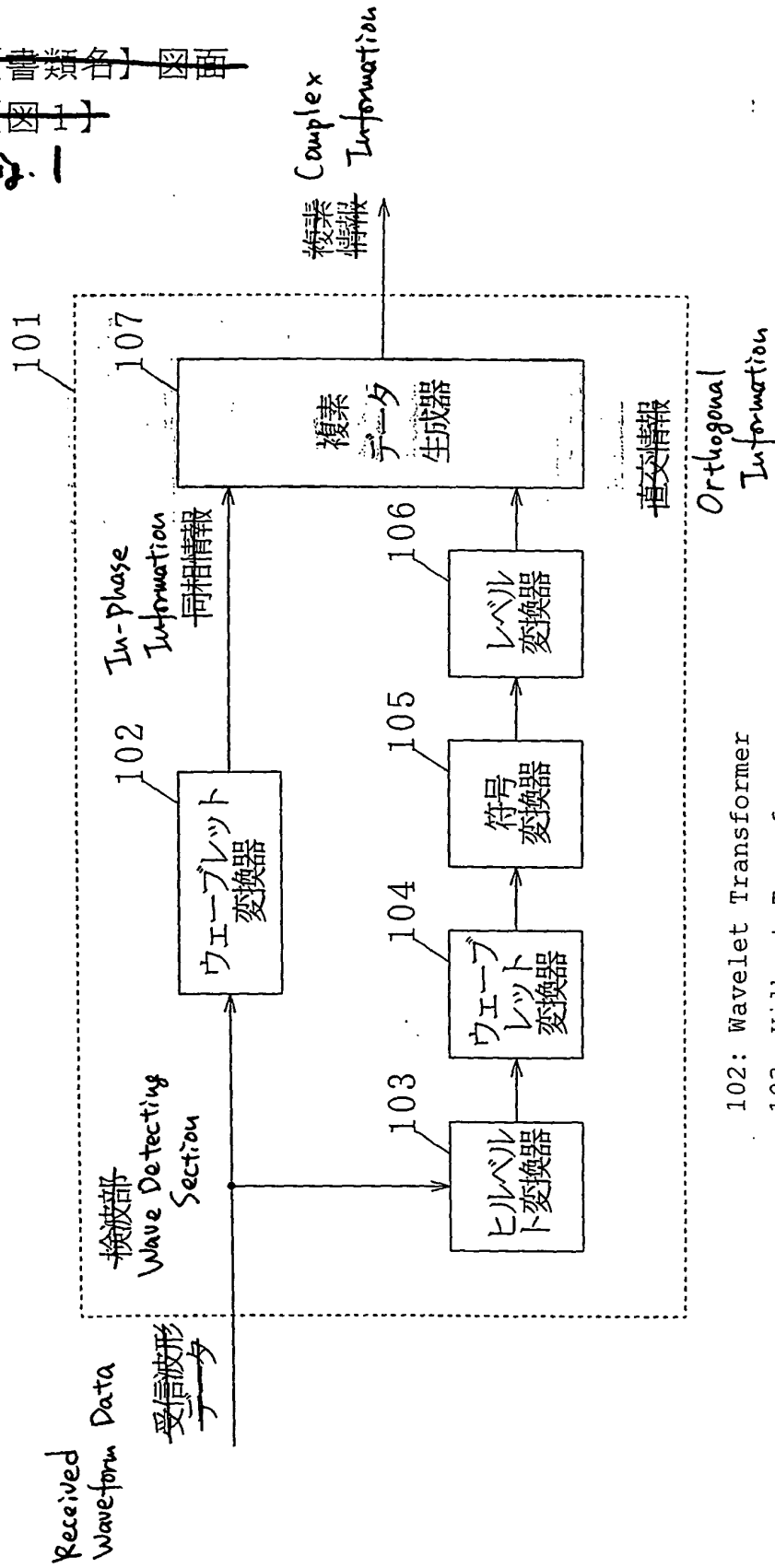
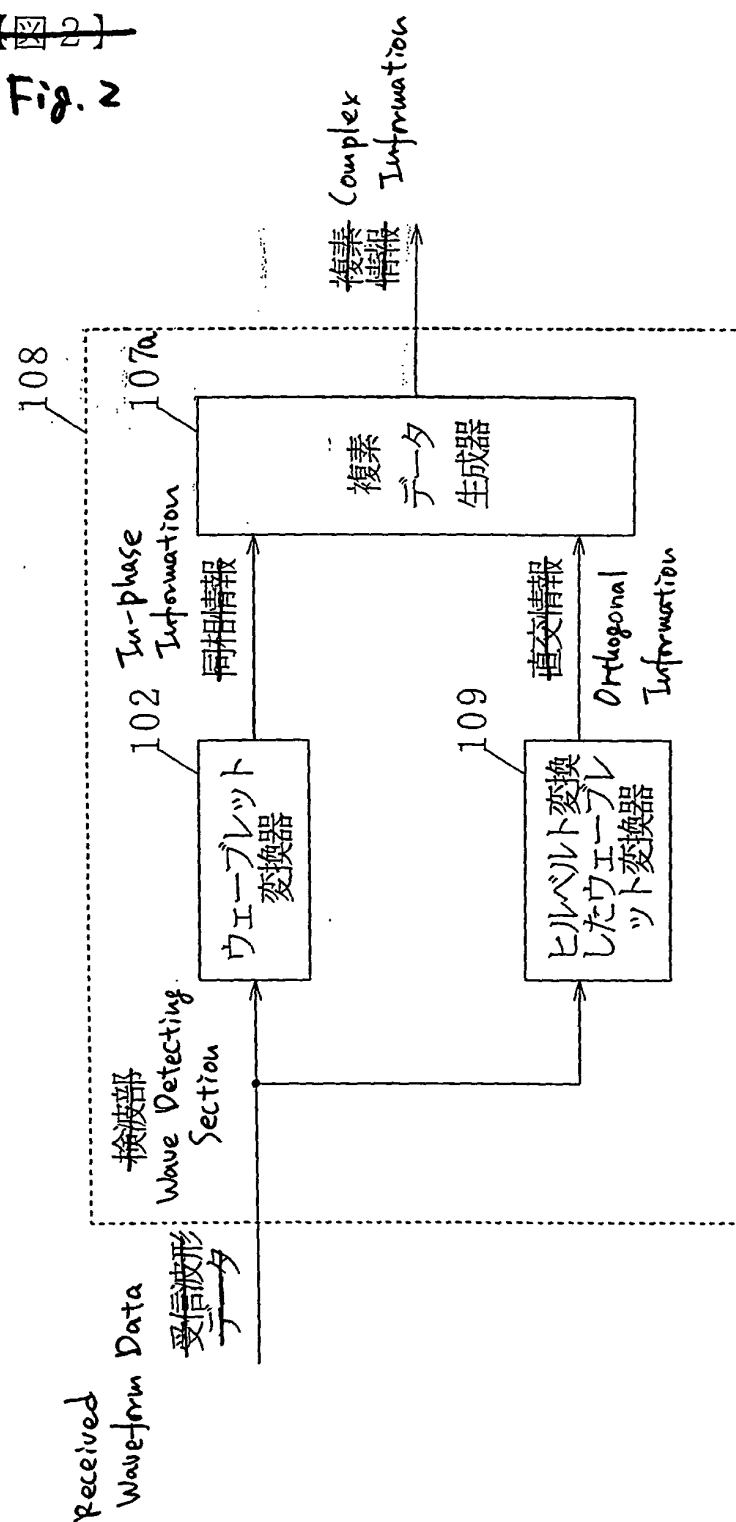


[書類名] 図面  
 [図 1]  
 Fig. 1



- 102: Wavelet Transformer
- 103: Hilbert Transformer
- 104: Wavelet Transformer
- 105: Code Converter
- 106: Level Converter
- 107: Complex Data Generator

Fig. 2



- 102: Wavelet Transformer
- 107: Complex Data Generator
- 109: Wavelet Transformer Performed Hilbert Transform

[ 3 ]

Fig. 3

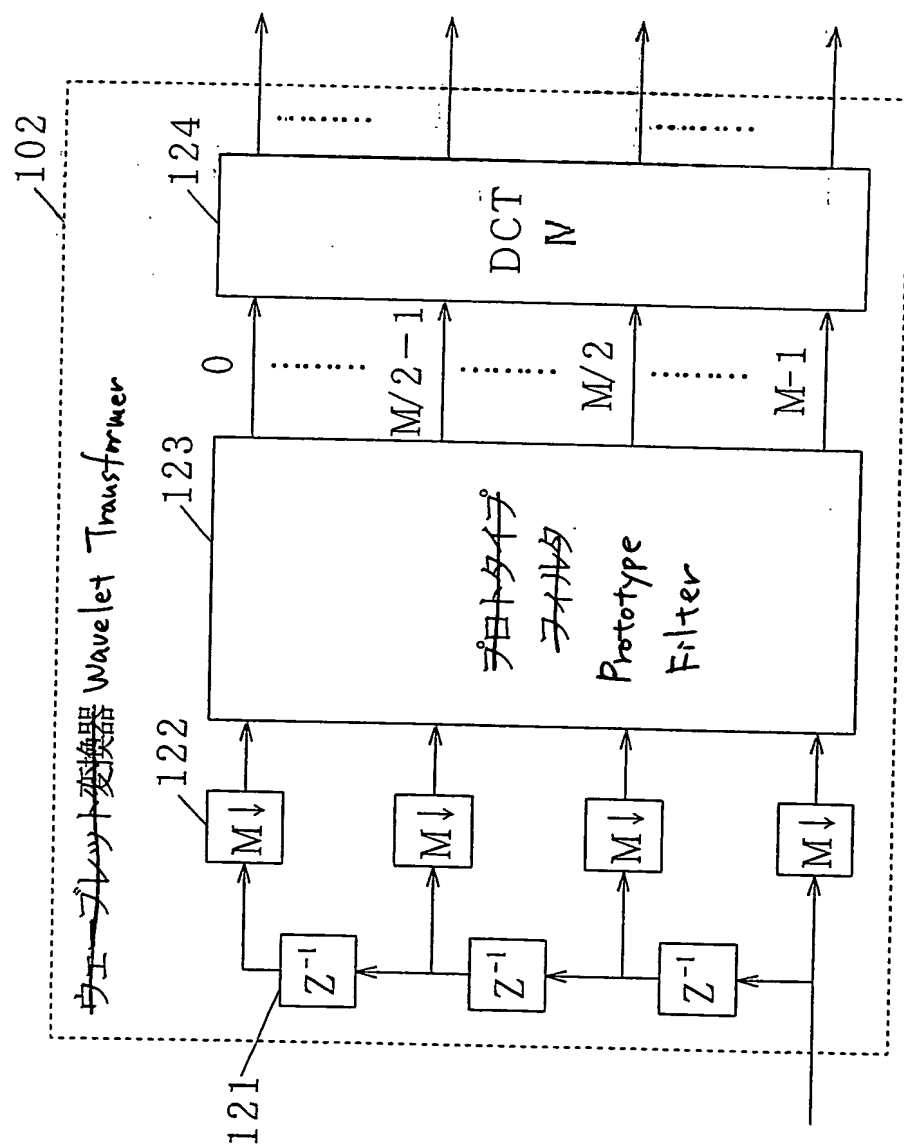


Fig. 4  
[4]

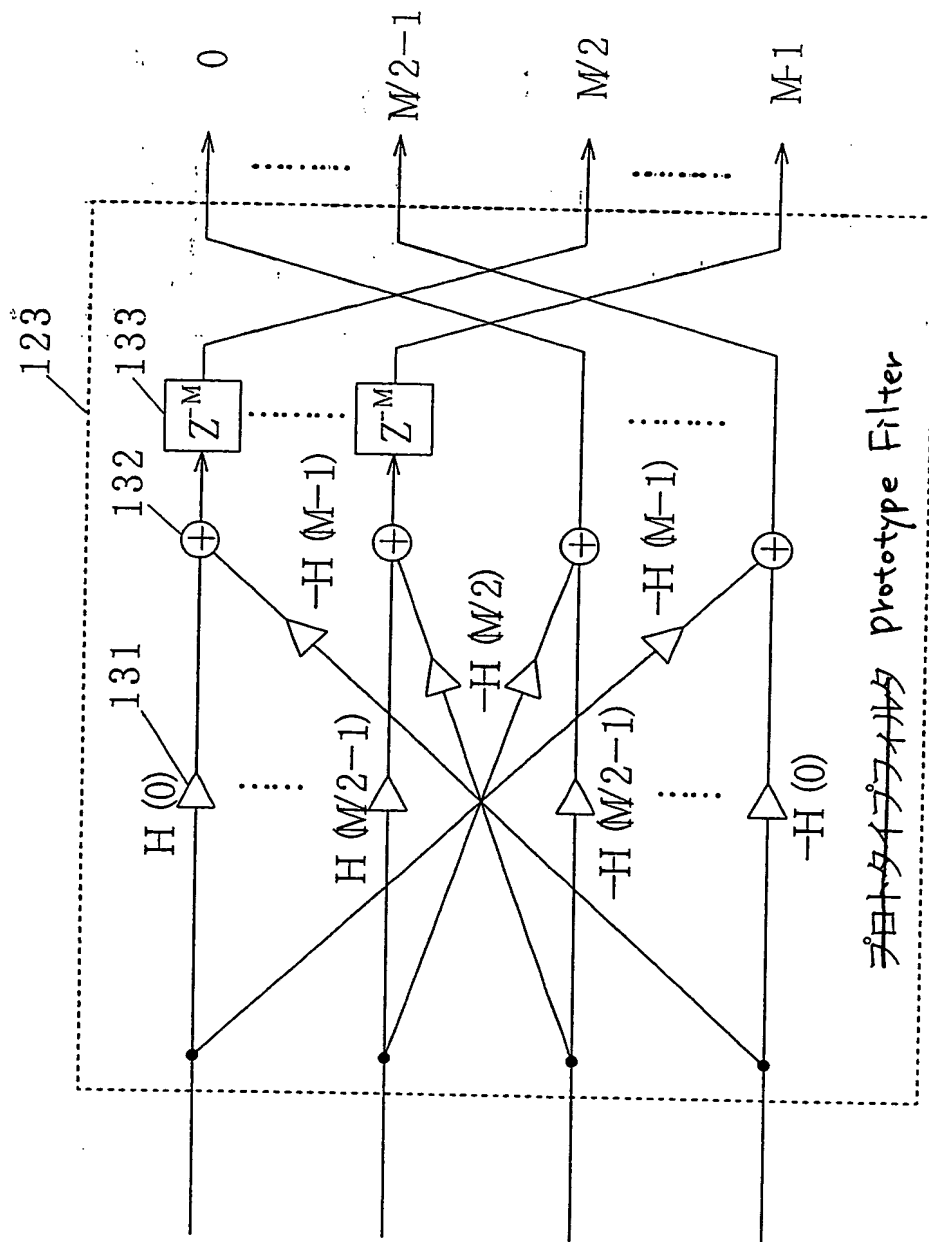


Fig. 5  
~~図 5~~

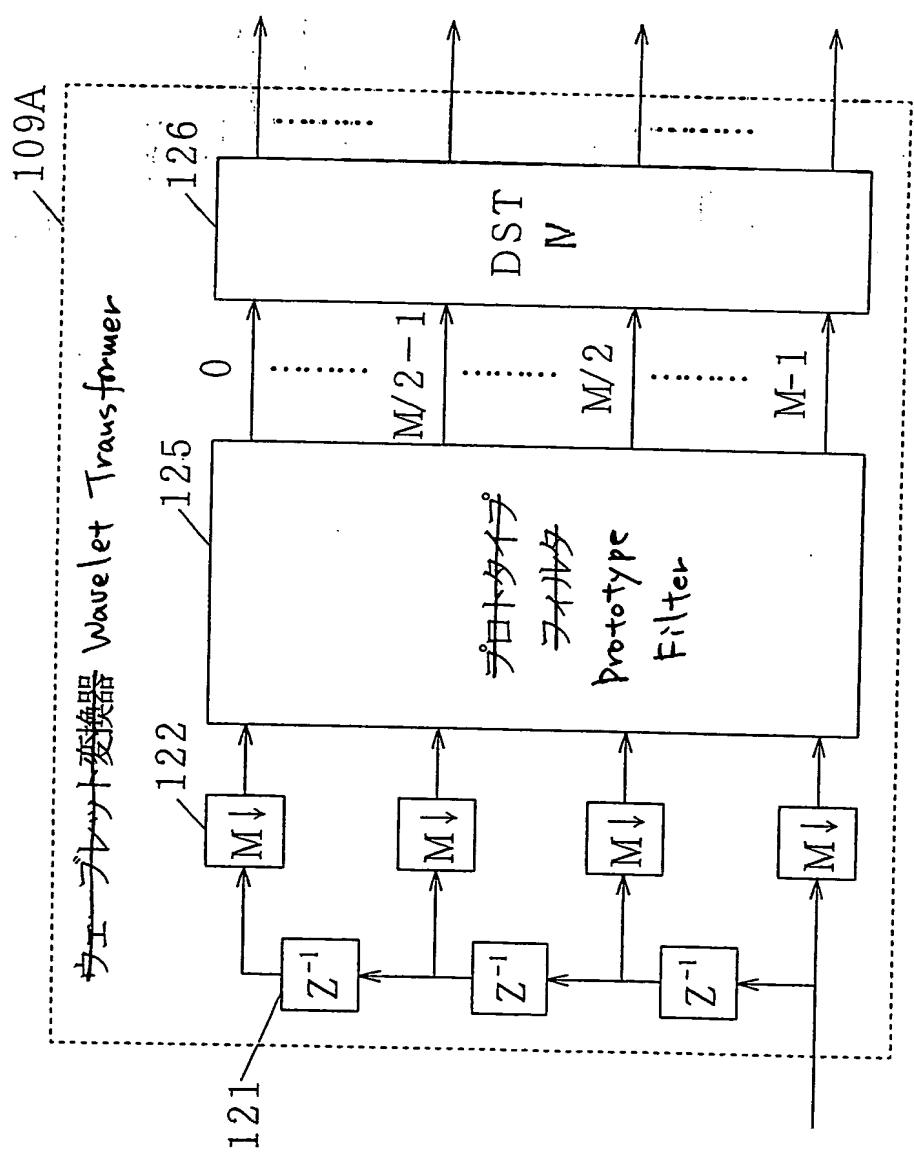


Fig. 6  
~~[ 6 ]~~

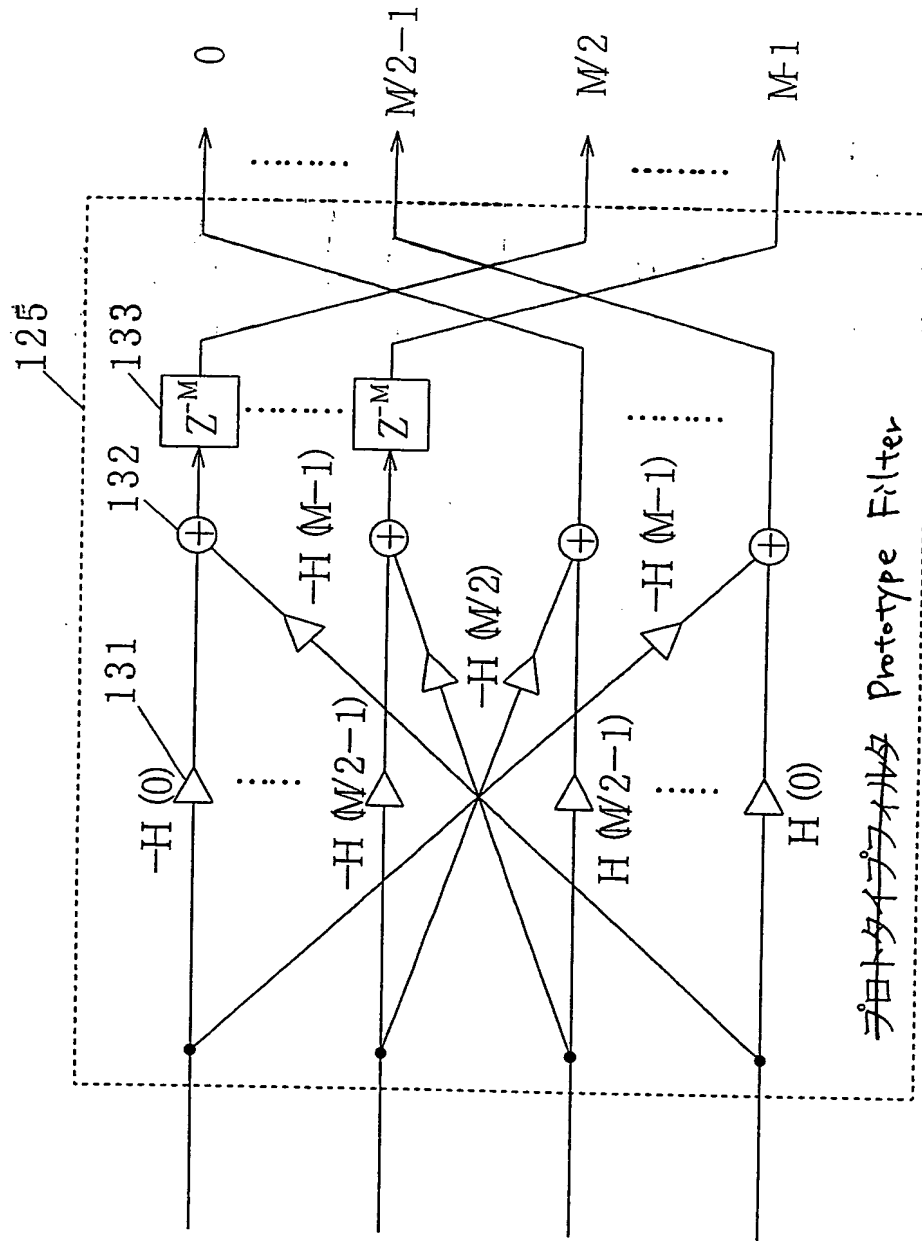
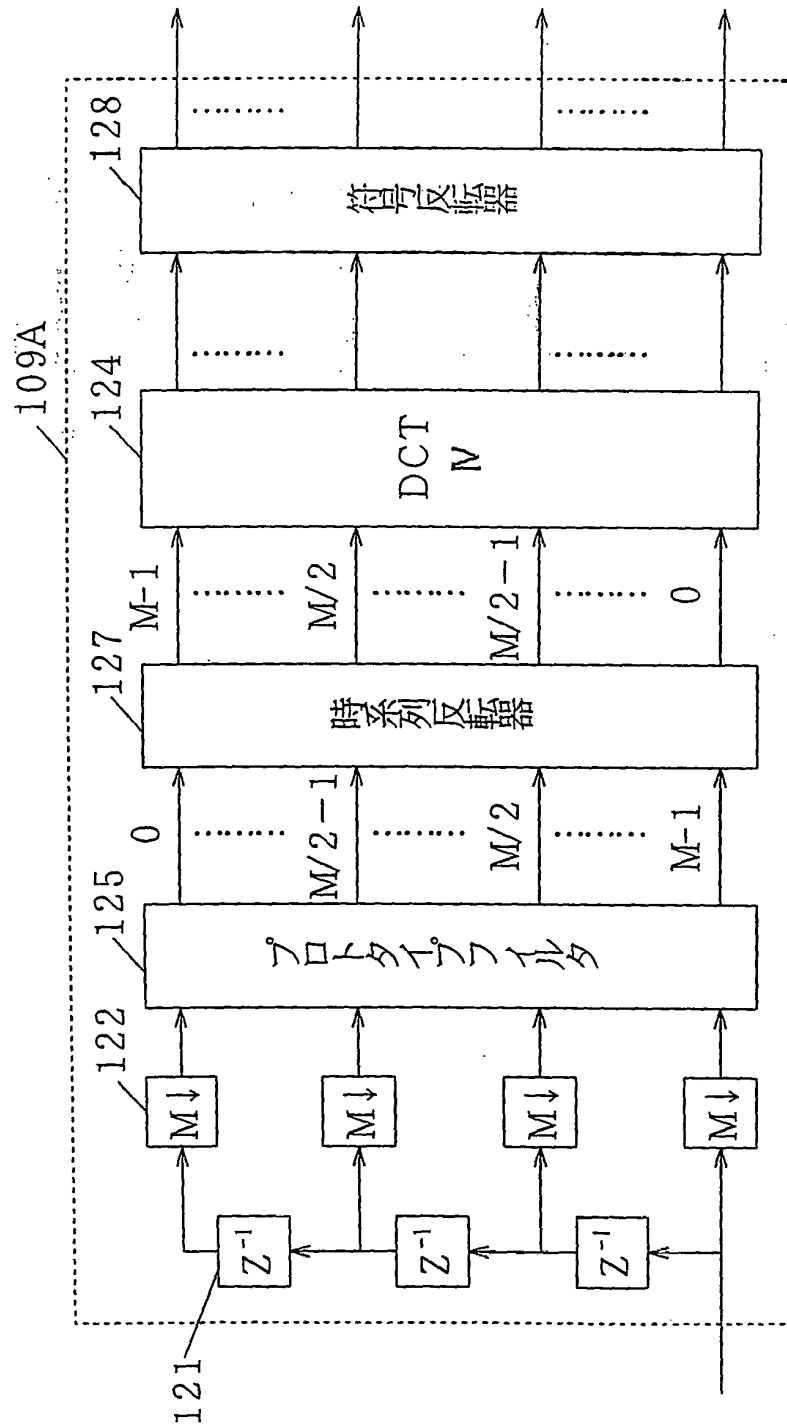


Fig. 7

[ 7 ]

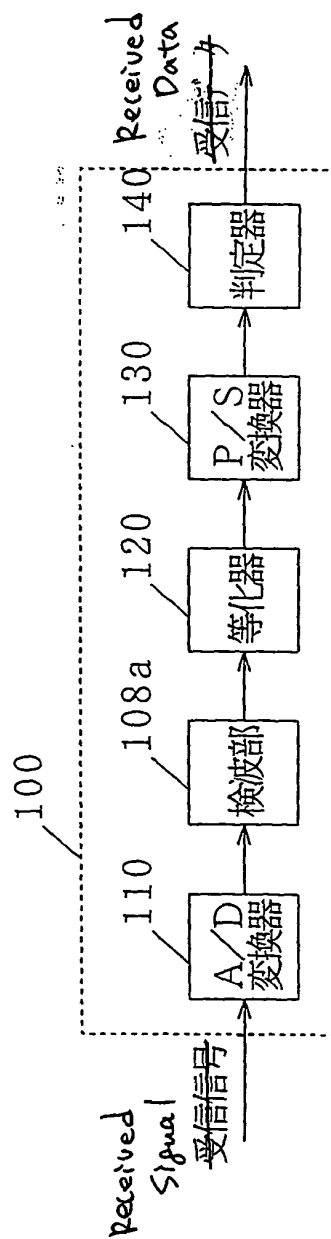


125: Prototype Filter

127: Time Series Inverter

128: Code Converter

Fig. 8  
[ 8 ]



- 110: A/D Converter
- 108a: Wave Detecting Section
- 120: Equalizer
- 130: P/S Converter
- 140: Judgment Unit



201: Synchronization Data Generator  
 210: Symbol Mapper  
 220: S/P Converter  
 230: Inverse Wavelet Transformer  
 240: D/A Converter

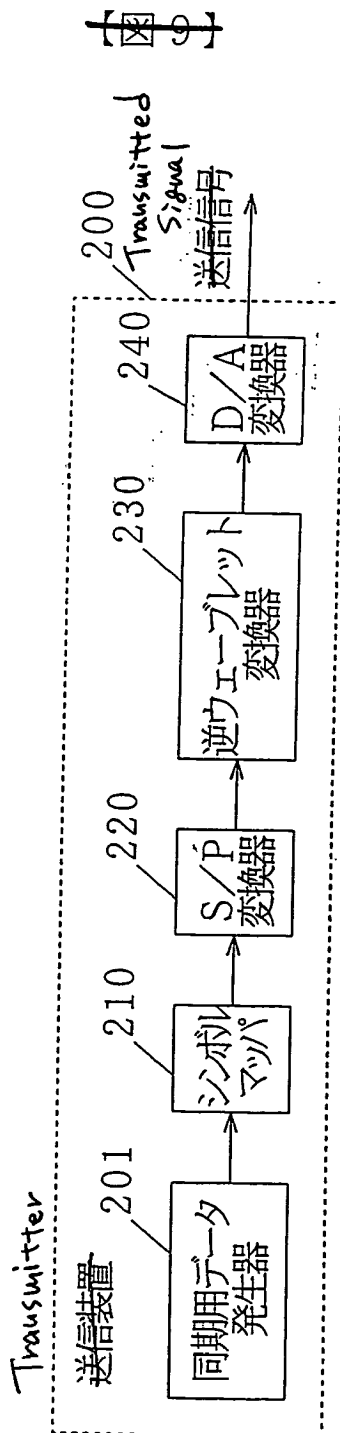


Fig. 9A

108a: Wave Detecting Section  
 110: A/D Converter  
 120: Equalizer  
 130: P/S Converter  
 140: Judgment Unit  
 142: Complex Divider  
 143: Complex Adder  
 144: Synchronization Shift Calculator  
 145: Synchronization Timing Estimation Circuit

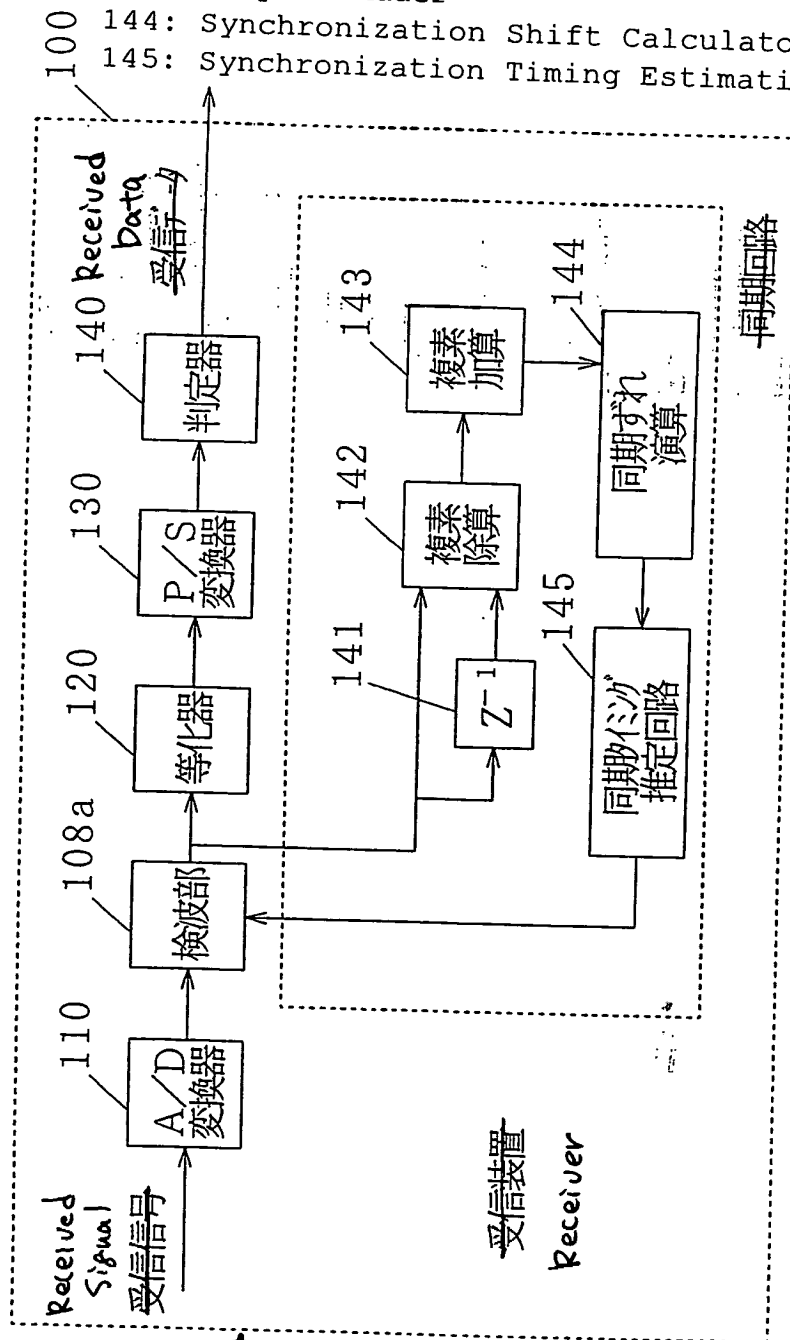
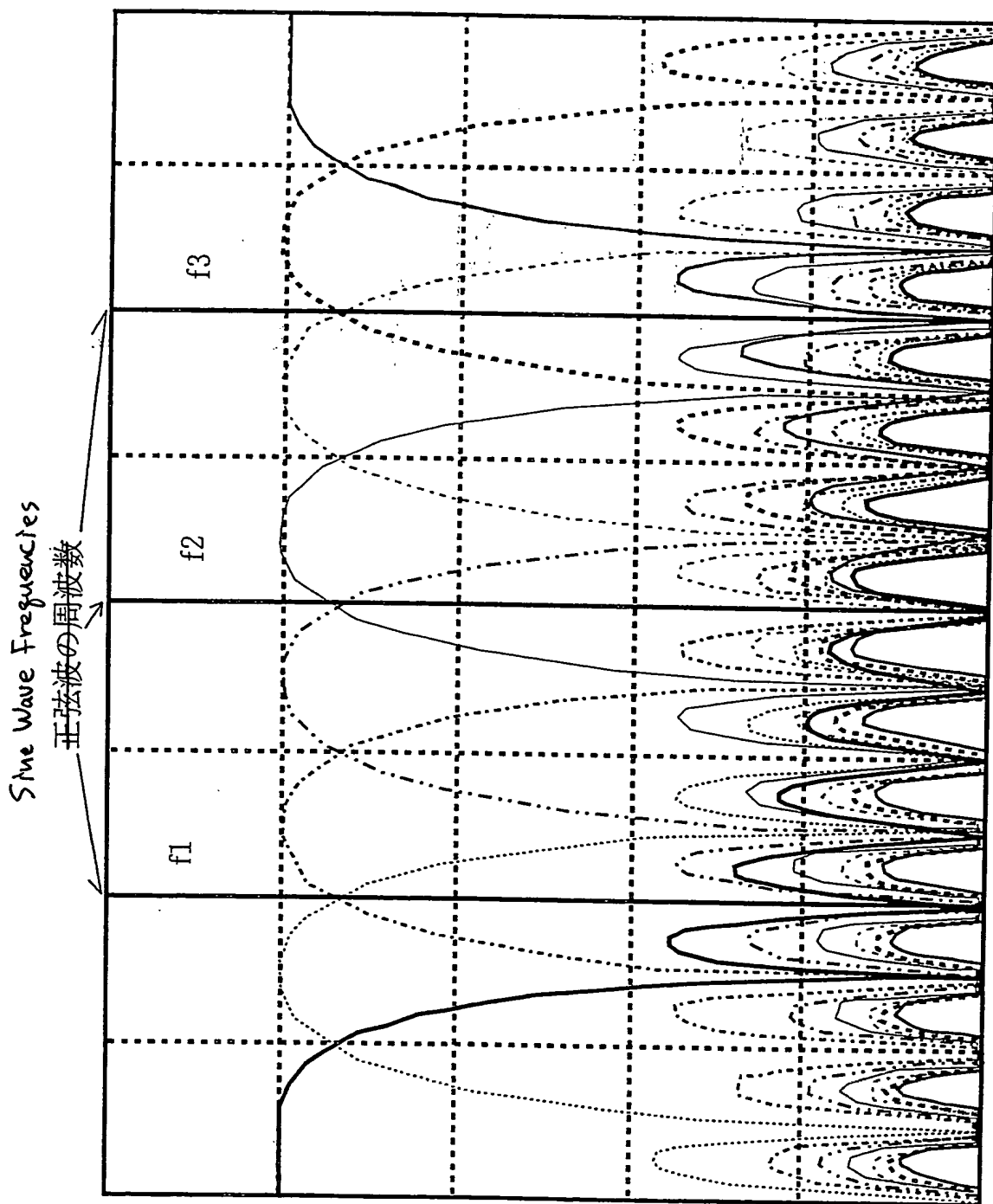


Fig. 9B

Synthesizing  
Circuit

Fig. 10

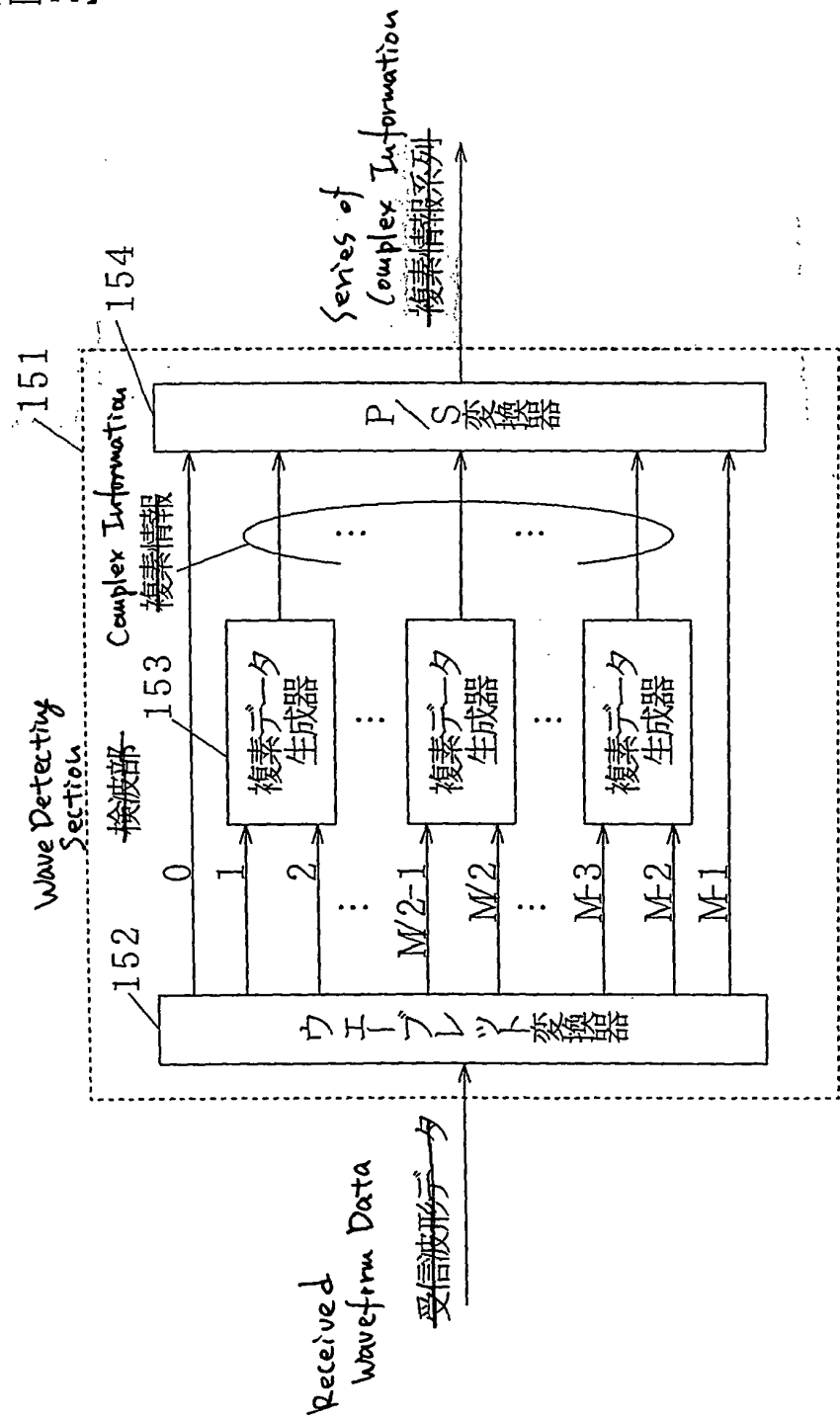
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サブキャリア番号は左側から50, 1, ..., 7

Subcarriers are numbered from 0 to 7 starting on left side.

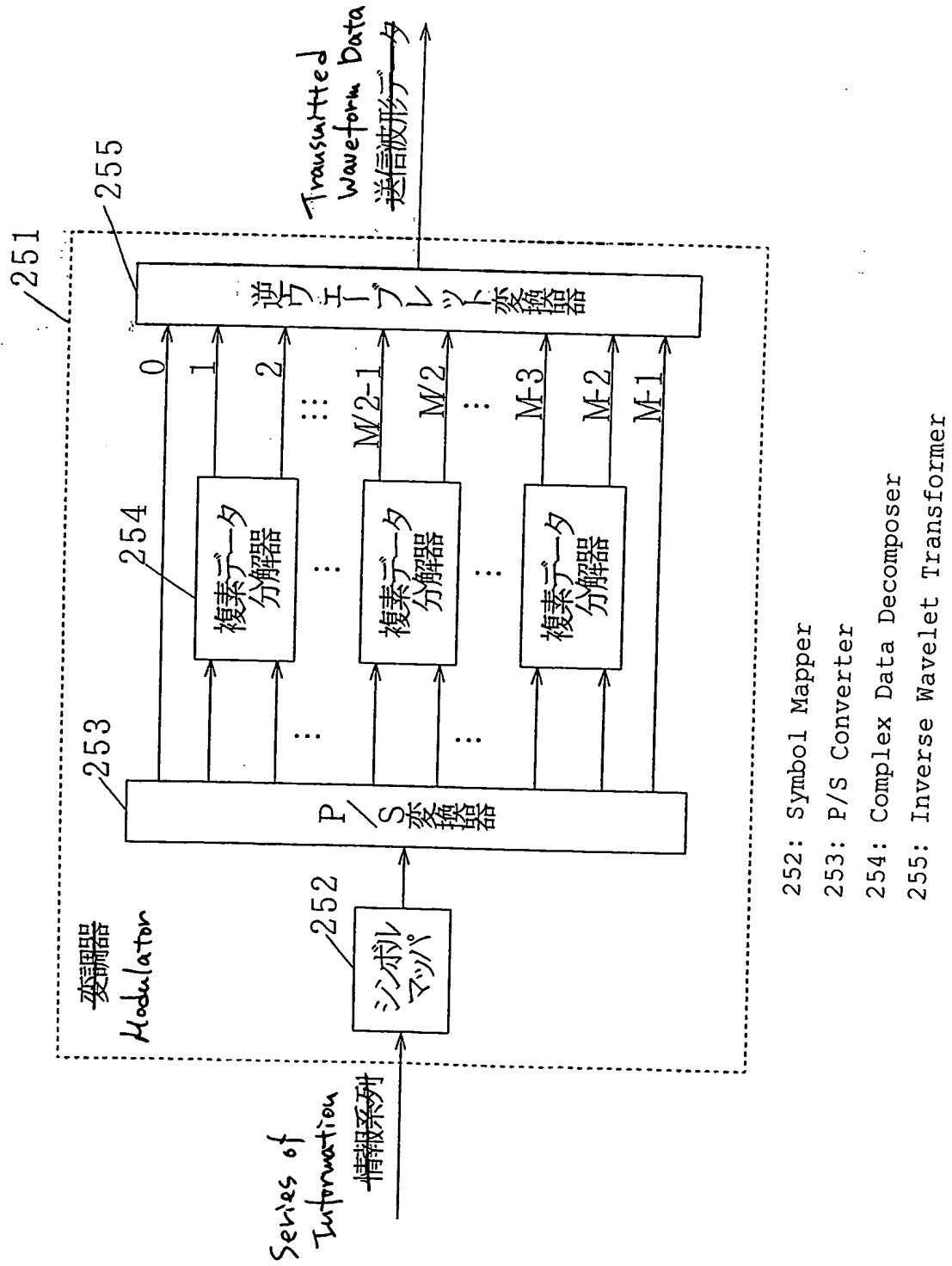
Fig. 11



- 152: Wavelet Transformer
- 153: Complex Data Generator
- 154: P/S Converter

Fig. 12

[図12]



252: Symbol Mapper

253: P/S Converter

254: Complex Data Decomposer

255: Inverse Wavelet Transformer

- 240: D/A Converter
- 251: Modulator
- 256: Synchronization Data Generator

(a) Fig. 13A

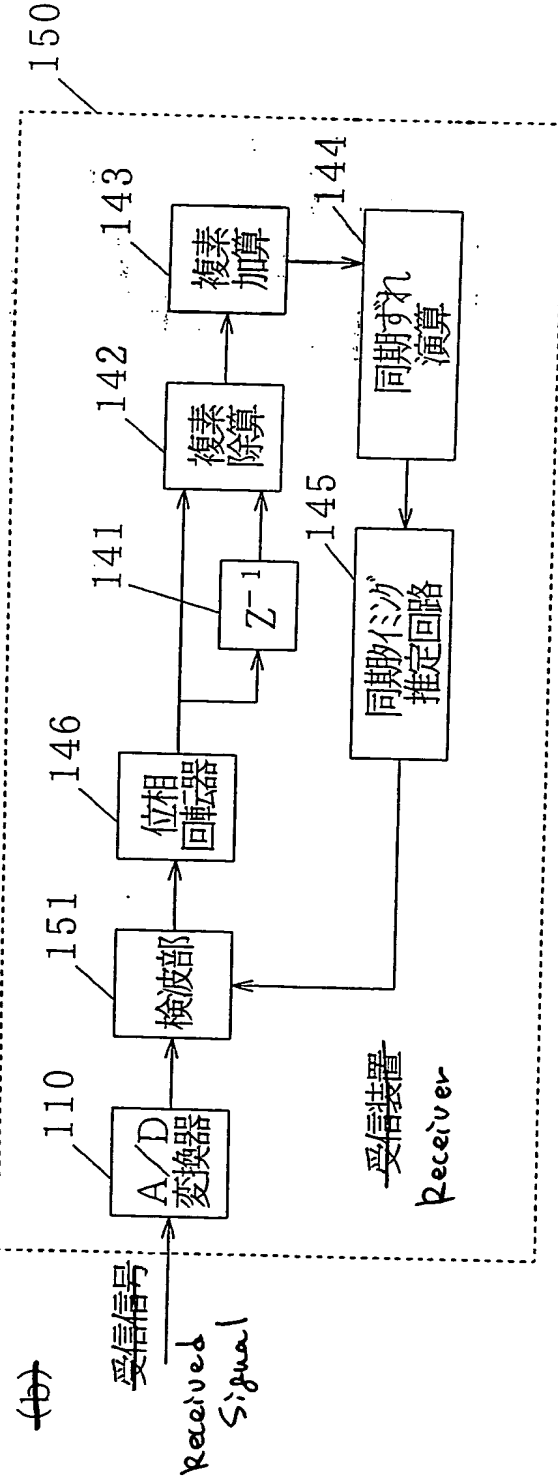
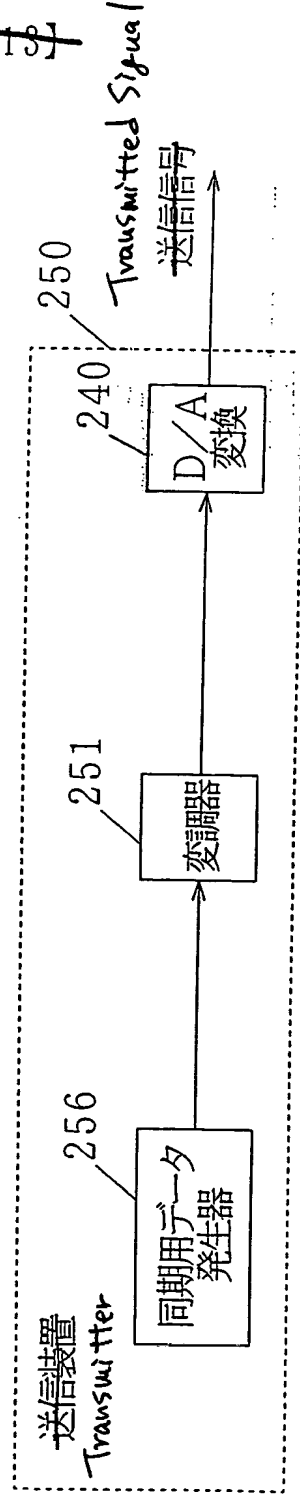
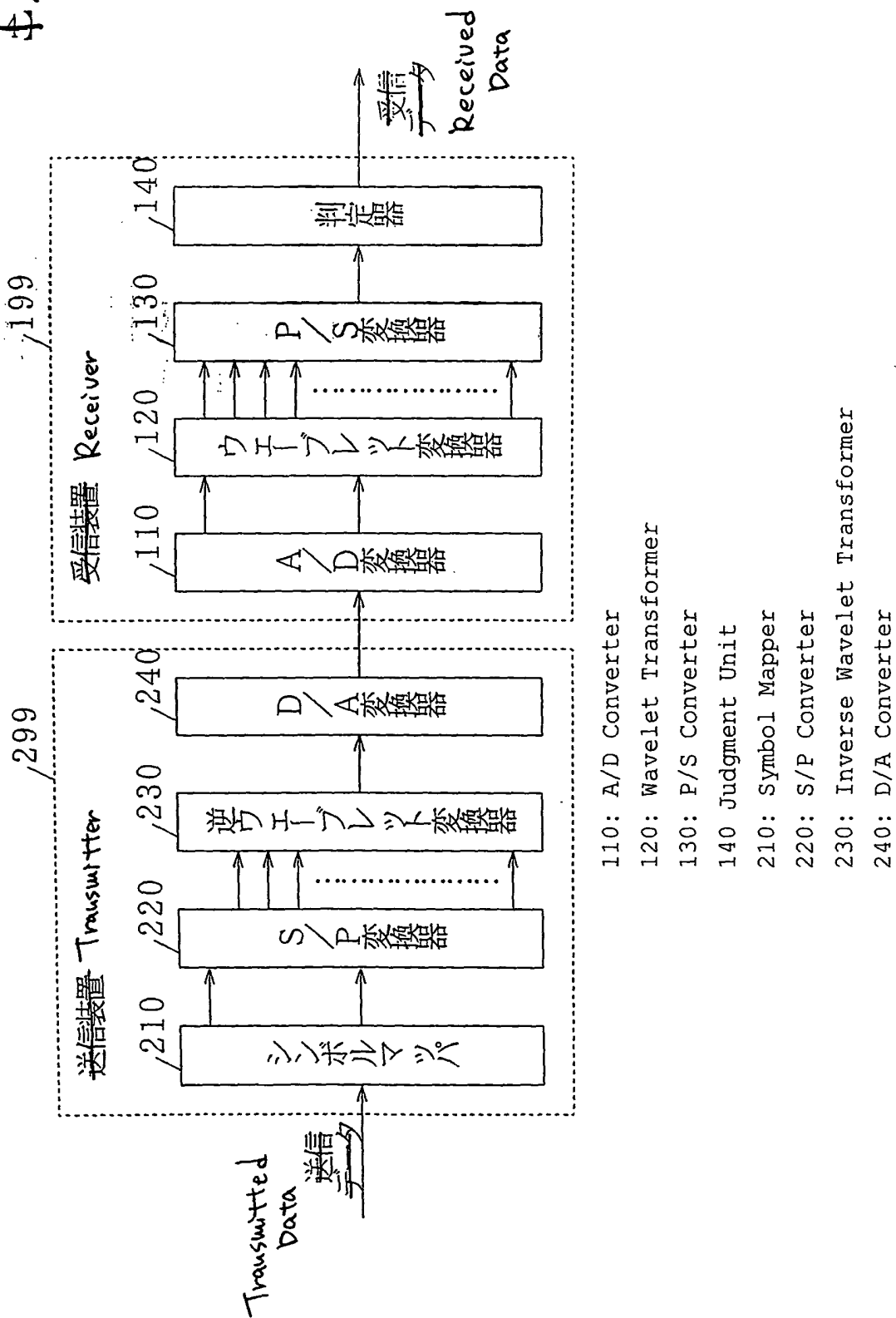


Fig. 13B

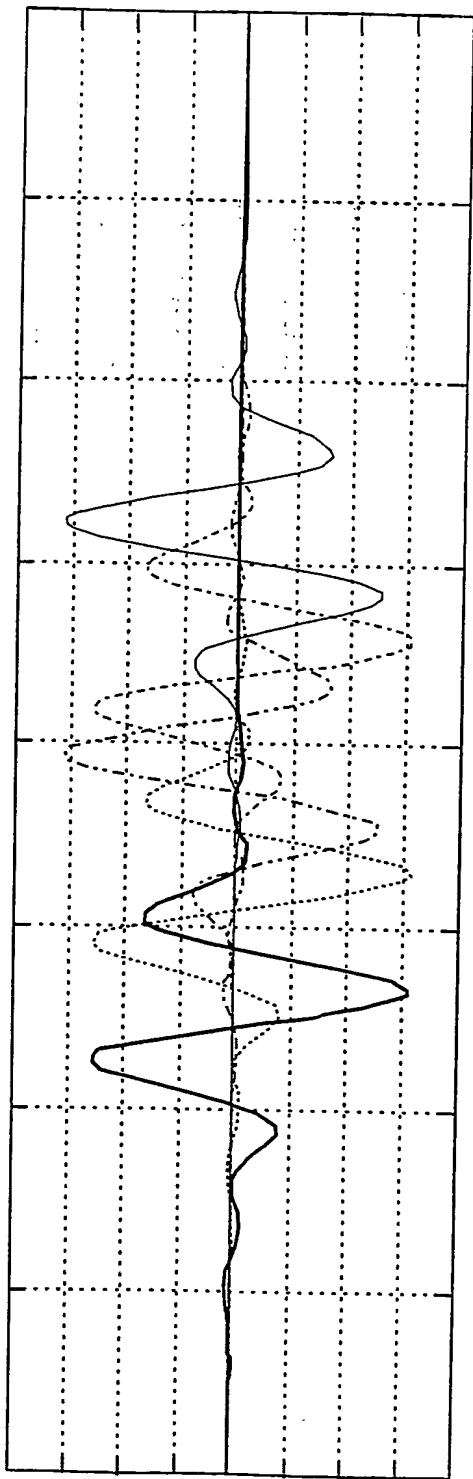
- 110: A/D Converter
- 142: Complex Divider
- 143: Complex Adder
- 144: Synchronization Shift Calculator
- 145: Synchronization Timing Estimation Circuit
- 151: Wave Detecting Section

Fig. 14  
[図14]



~~[X] 15~~

Fig.15



~~16~~

Fig. 16

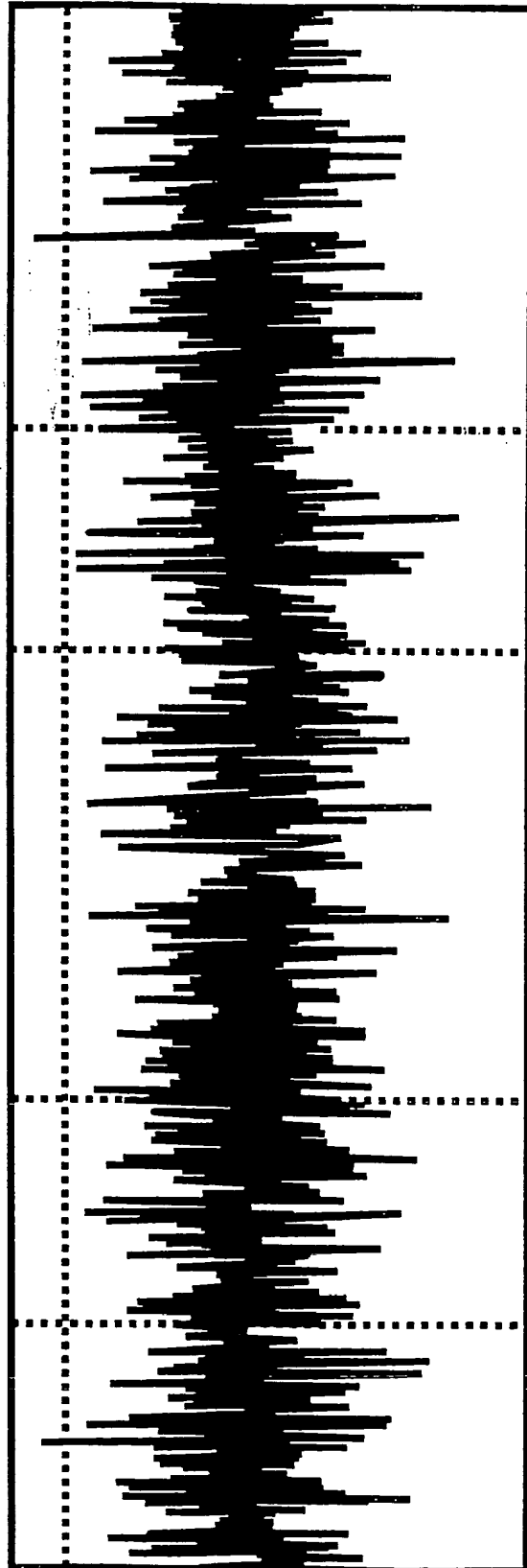
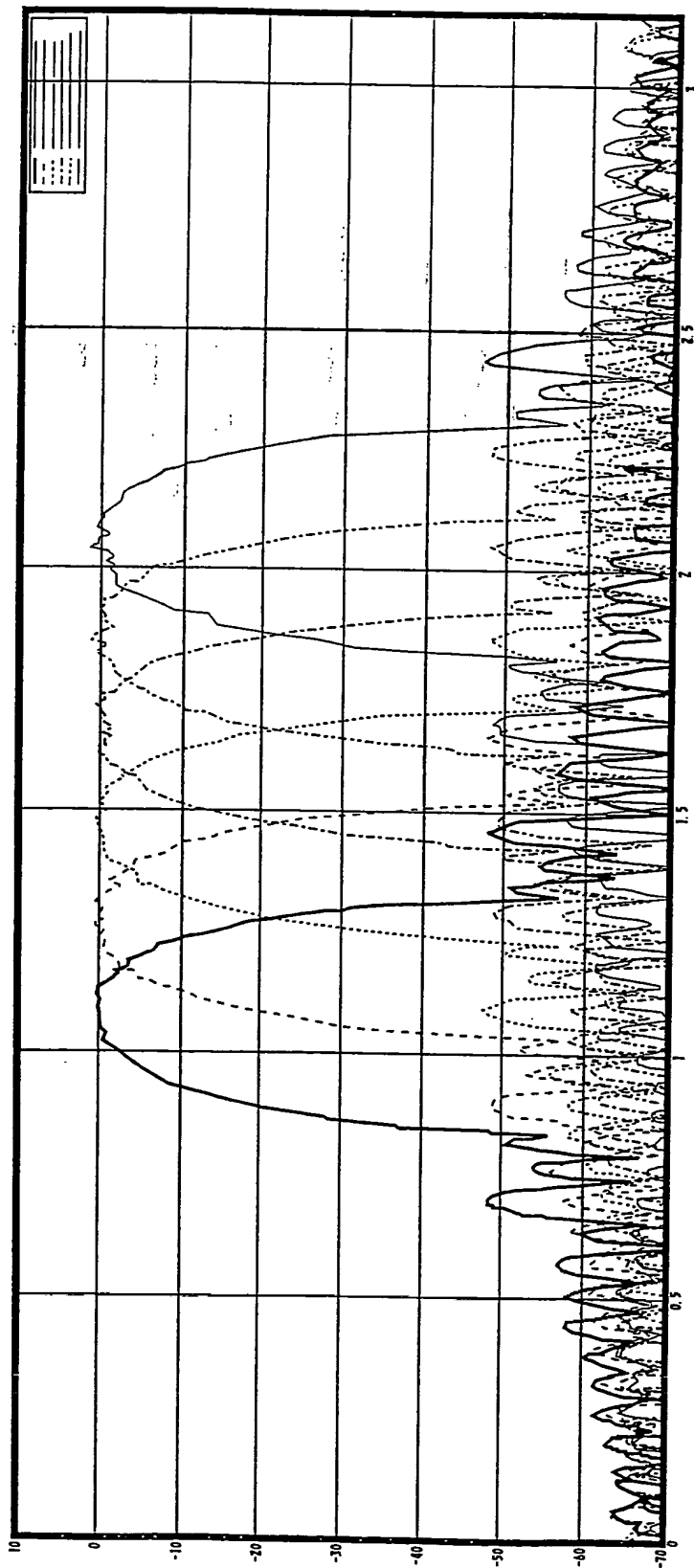




Fig. 17

~~(X) 17~~



~~【図18】~~

Fig. 18

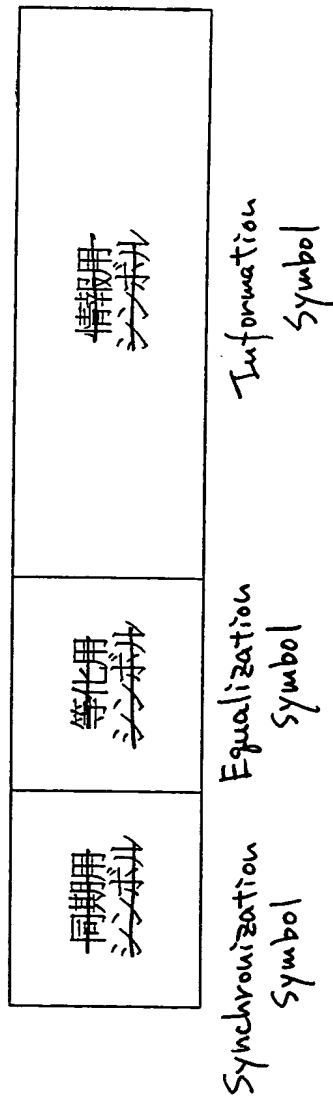
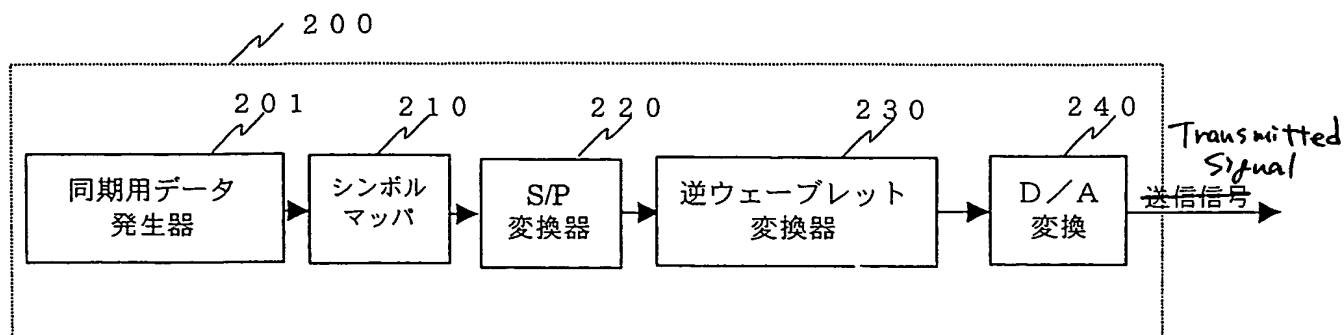


Fig. 19

~~Fig. 19~~



201: Synchronization Data Generator

210: Symbol Mapper

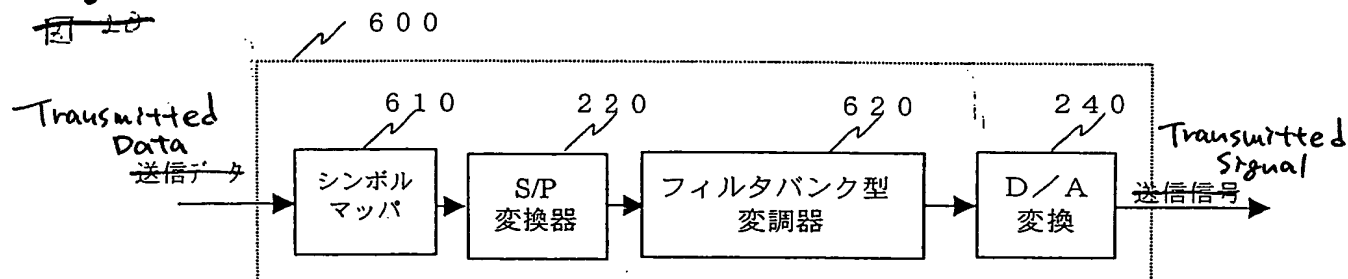
220: S/P Converter

230: Inverse Wavelet Transformer

240: D/A Converter

Fig. 20

~~Fig. 20~~



220: S/P Converter

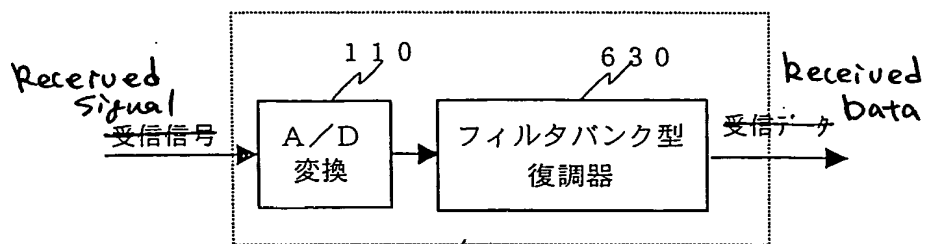
240: D/A Converter

610: Symbol Mapper

620: Filter Bank Type Converter

~~Fig. 21~~

Fig. 21



110: A/D Converter

630: Filter Bank Type Demodulator

~~Fig. 22~~

Fig. 22

- 700: Communication Apparatus
- 720: Audio Visual Apparatus
- 730: Documentation Apparatus
- 740: Security Apparatus
- 750: Personal Computer
- 760: Home Electric Apparatus
- 810: Power Line

